

**DEPARTMENT OF COMPUTER SCIENCE**

**LAB ASSIGNMENT # 8**

**Title of the Assignment**

TO VERIFY THE OPERATIONS FOR HALF ADDER CIRCUIT.  
TO VERIFY THE OPERATIONS FOR FULL ADDER CIRCUIT.  
TO CONSTRUCT A FULL ADDER USING 2 HALF ADDERS AND VERIFY THE OPERATIONS.

**Submitted by**

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| Name of the student | Class Roll. No. | Section |
| Joon Shakya | 021BSCIT012 | A |

**Submitted To**

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| --- | --- | --- |
| Name of the Faculty | Signature | Remarks |
| Er. Rajan Karmacharya |  |  |
| Er. Rabin Maharjan |  |  |

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